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CONFIRMATION NO. 1570

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|---|---|----------------------------------|---|--|
| <b>SERIAL NUMBER</b><br>10/500,931  | <b>FILING OR 371(c) DATE</b><br>07/08/2004<br><b>RULE</b>   | <b>CLASS</b><br>438              | <b>GROUP ART UNIT</b><br>2812   | <b>ATTORNEY DOCKET NO.</b><br>501.43797X00 |
| <b>APPLICANTS</b><br>Hiroaki Kikuchi, Chitose, JAPAN;<br>Toshiaki Sawada, Koganei, JAPAN;<br>Hirohiko Yamamoto, Hachioji, JAPAN;  |   |                                  |   |  |
| <b>** CONTINUING DATA *****</b><br>This application is a 371 of PCT/JP03/00068 01/08/2003   |   |                                  |   |  |
| <b>** FOREIGN APPLICATIONS *****</b><br>JAPAN 2002-2507 01/09/2002  |   |                                  |   |  |
| Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no<br>35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after<br>met Allowance<br>Verified and Acknowledged _____<br>Examiner's Signature Initials |   | <b>STATE OR COUNTRY</b><br>JAPAN | <b>SHEETS DRAWING</b><br>12   | <b>TOTAL CLAIMS</b><br>13                  |
| <b>INDEPENDENT CLAIMS</b><br>8  |   |                                  |   |  |
| <b>ADDRESS</b><br>020457  |   |                                  |   |  |
| <b>TITLE</b><br>Semiconductor integrated circuit device manufacturing method  |   |                                  |   |  |
| <b>FILING FEE RECEIVED</b><br>1350  | FEES: Authority has been given in Paper<br>No. _____ to charge/credit DEPOSIT ACCOUNT<br>No. _____ for following: |                                  | <input type="checkbox"/> All Fees<br><input type="checkbox"/> 1.16 Fees ( Filing )<br><input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )<br><input type="checkbox"/> 1.18 Fees ( Issue )<br><input type="checkbox"/> Other _____<br><input type="checkbox"/> Credit |  |